

Electronic Acknowledgement Receipt

EFS ID:	1192675
Application Number:	10085694
Confirmation Number:	1627
Title of Invention:	METHOD FOR PROCESSING MULTIPLE SEMICONDUCTOR DEVICES FOR TEST
First Named Inventor:	Gary J. Kovar
Customer Number:	23125
Filer:	Joanna Gariazzo Chiu/Pat Thomas
Filer Authorized By:	Joanna Gariazzo Chiu
Attorney Docket Number:	SC11763TK
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Payment was successfully received in RAM	\$ 100
RAM confirmation Number	331
Deposit Account	503079
The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:	
Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Request for Certificate of Correction	SC11763TK-USA-20060908-Cert-of-Correction.pdf	62286	no	2
Warnings:					
Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	8164	no	2
Warnings:					
Information:					
Total Files Size (in bytes):					70450

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.